

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES
SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865

Filed:

December 5, 2003

Examiner: Van Thu T. Nguyen

Group Art Unit: 2824

Atty. Docket No.: 023-0029

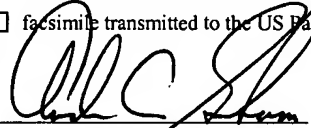
Confirmation No.: 8494

December 28, 2006

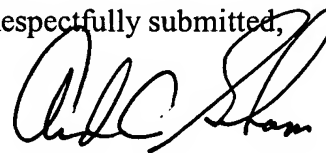
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF
PATENT APPEALS AND INTERFERENCES**

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the last decision of the Examiner.

The fee for this Notice of Appeal under 37 C.F.R. § 41.20(b)(1) is submitted as set forth in the accompanying transmittal letter.

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 Andrew C. Graham	<u>12/28/06</u> Date

Respectfully submitted,

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